



PTO/SB/08A (10-01)
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Substitute for form 1449A/PTO		Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application / Conf. No.	10/805,116 / 8509		
		Filing Date	March 19, 2004		
		First Named Inventor	Eric J. Crabill		
		Art Unit	2819 2183		
		Examiner Name	Unknown W. TREAT		
Sheet	1	of	4	Attorney Docket Number	X-841-1C US

U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No. *	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)				
WMT		US-	Re 3,363	08-03-93	Freeman	
		US-	4,314,353	02-02-82	Gunter et al.	
		US-	4,789,951	12-06-88	Birkner et al.	
		US-	4,870,302	09-26-89	Freeman	
		US-	5,093,915	03-03-92	Platteter et al.	
		US-	5,140,193	08-18-92	Freeman et al.	
		US-	5,206,940	04-27-93	Murakami et al.	
		US-	5,343,406	08-30-94	Freeman et al.	
		US-	5,347,641	09-13-94	Cedar et al.	
		US-	5,369,314	11-29-94	Patel et al.	
		US-	5,402,014	03-28-95	Ziklik et al.	
		US-	5,432,719	07-11-95	Freeman et al.	
		US-	5,432,950	07-11-95	Sibigtroth	
		US-	5,448,493	09-05-95	Topolewski et al.	
		US-	5,488,316	01-30-96	Freeman et al.	
		US-	5,504,439	04-02-96	Tavana	
		US-	5,592,102	01-07-97	Lanc et al.	
		US-	5,635,851	06-03-97	Tavana	
		US-	5,682,107	10-28-97	Tavana et al.	
	WMT		US-	5,710,891	01-20-98	Normoyte et al.

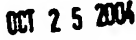
FOREIGN PATENT DOCUMENTS							
Examiner Initials *	Cite No. *	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T *
		Country Code *	-Number * -Kind Code * (if known)				
WMT			EP 0 062 431 A1	03-19-82	Fujitsu Limited		
			EP 0 139 254 A2	09-28-84	IBM		
			EP 0 307 649 A2	08-19-88	Compaq Computer Corp.		
			EP 0 306 962 A2	09-08-88	Hitachi, Ltd.		
			EP 0 361 525 A2	09-29-89	Hitachi, Ltd.		
			EP 0 486 248 A2	11-12-91	Xilinx, Inc.		
			EP 0 503 498 A2	03-06-92	Oki Electric Industry Co		
			EP 0 511 674 A2	04-30-92	Kabushiki Kaisha Toshiba		
			EP 0 536 793 A2	10-09-92	Kabushiki Kaisha Toshiba		
WMT			EP 0 636 976 A1	07-21-94	Philips Electronics N.V.		

Examiner Signature	W. TREAT	Date Considered	5/12/05
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Filing Date				March 19, 2004			
First Named Inventor				Eric J. Crabill			
Art Unit				2819 2143			
Examiner Name				Unknown W. TREAT			
Attorney Docket Number				X-841-1C US			
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WMT		US- 5,748,912	05-05-98	Lee	
		US- 5,774,684	06-30-98	Haines et al.	
		US- 5,784,637	07-21-98	Sawase et al.	
		US- 5,818,255	10-06-98	New et al.	
		US- 5,825,202	10-20-98	Tavana et al.	
		US- 5,834,947	11-10-98	Cedar et al.	
		US- 5,844,424	12-01-98	Krishnamurthy et al.	
		US- 5,844,854	12-01-98	Lee	
		US- 5,847,580	12-08-98	Bapat et al.	
		US- 5,880,597	03-09-99	Lec	
		US- 5,883,525	03-16-99	Tavana et al.	
		US- 5,901,295	05-04-99	Yazdy	
		US- 5,911,082	06-08-99	Monroe et al.	
		US- 5,935,230	08-10-99	Pinai et al.	
		US- 5,936,424	08-10-99	Young et al.	
		US- 5,942,913	08-24-99	Young et al.	
		US- 5,963,050	10-05-99	Young et al.	
		US- 5,982,195	11-09-99	Cliff et al.	
		US- 6,020,757	02-01-00	Jenkins et al.	
WMT		US- 6,020,758	02-01-00	Patel et al.	

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Examiner Signature	W. TREAT	Date Considered	5/12/85
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WMT		US- 6,023,742	02-08-00	Ebeling et al.	
		US- 6,067,615	05-23-00	Upton	
		US- 6,085,317	07-04-00	Smith	
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		US- 6,107,824	08-22-00	Reddy et al.	
		US- 6,107,827	08-22-00	Young et al.	
		US- 6,184,705 B1	02-06-01	Cliff et al.	
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		US- 6,212,639 B1	04-03-01	Erickson et al.	
		US- 6,236,245 B1	05-22-01	Papaliolios	
		US- 6,259,286 B1	07-10-01	Papaliolios	
		US- 6,282,627 B1	08-28-01	Wong et al.	
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Sheet	4	of	4	Attorney Docket Number	X-841-1C US

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T *
WMT		IBM; Technical Disclosure Bulletin; "System Master Slice for Fast Turnaround Time"; Vol. 26, No. 3B; August 1983; pp. 1531-1532.	
		IBM; Technical Disclosure Bulletin; "Update Mechanism for Personal Computer System Resident Firmware"; Vol. 34, No. 10B; March 1992; pp. 133-136.	
		IBM; Technical Disclosure Bulletin; "Protocol Extensions to Microprocessor Memory Bus to Support Extend Extended Address Space"; Vol. 37, No. 05; May 1994; pp. 389.	
WMT		IBM; Technical Disclosure Bulletin; "Efficient Mechanism for Multiple Debug Modes"; Vol. 38, NO. 11; November 1995; pp. 65-68.	

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